

SN	CRSCODE	QUESTION
1	CIT236	The first stage involved in the process of rectification is the _____
2	CIT236	The conversion _____ of rectification is given by the ratio of
3	CIT236	The ratio of the rms value of AC components to the DC value of load
4	CIT236	In the Series Derived Shunt-Fed Feedback Topology the input is connected
5	CIT236	The following are major applications of Zener diode except
6	CIT236	LM317 is an example of a _____ terminal adjustable regulator
7	CIT236	Karnaugh map uses a _____ divided into rows and columns in such a way
8	CIT236	_____ Is the standard language for all database
9	CIT236	_____ takes a scaled version of the output and compares it with the reference
10	CIT236	_____ control or maintain a constant dc voltage output by controlling the duty cycle

OPTION A	OPTION B	OPTION C	OPTION D	ANS	TMA NO
Transformer st	Rectifying sta	Filtering Stage	Voltage Divid	A	TMA3
Output	Reliability	Efficiency	Capacity	C	TMA3
Voltage Regula	Form Factor	Ripple Factor	Rectification	D	TMA3
series	parallel	serial	linear	B	TMA3
Voltage Regula	Voltage Limit	Meter Protecti	Voltage Conv	D	TMA3
two	three	four	six	B	TMA3
square	circle	rectangle	triangle	C	TMA3
V.B	java	O.S	SQL	D	TMA3
Error amplifier	Voltage refer	list	line	A	TMA3
series file	series tool	regulators	Series regula	D	TMA3